

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet 1 of 2

Complete to Known

Application Number	Not Yet Assigned
Filing Date	September 19, 2006
First Named Inventor	Richard Ostrcil
Art Unit	Not Yet Assigned
Examiner Name	Not Yet Assigned
Attorney Docket Number	2004P00886WOUS

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US- 6,647,427 B1	11-11-2003	Watanabe	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	2	WO 01/35601 A1	05-17-2001	RAIN-FINITY, INC.		
	3	WO 00/52906 A1	09-08-2000	AT & T CORPORATION		
	4	EP 0865 180 A2	09-16-1998	LUCENT TECHNOLOGIES INC.		

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS); title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	5	LOUIS HARIK and AYMAN KAYSSI, "FPGA-Based Load Balancer for Internet Servers", IEEE Xplore, Microelectronics, The 14 th International Conference on 2002-ICM, December 11-13, 2002, Pages 1-5, Retrieved from Internet on May 5, 2004.	

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